



ROMWBW

User Guide

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RetroBrew Computing Group
www.retrobrewcomputers.org

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Section 1

Overview

RomWBW is system software that supports the Z80/Z180 based computing platforms produced by the RetroBrew Computing Group which is found at <http://www.retrobrewcomputers.org>. The goal of the project is to provide all firmware and software needed to make a fully functional computing platform. This includes both firmware (ROM) and software (disk images). The source code is provided and is licensed under GPL v3. A GitHub repository is used to maintain all source code and can be found at <http://www.github.com/RomWBW>.

Essentially all Z80/Z180 based hardware produced by RetroBrew Computing is fully supported by RomWBW. Much of the software was adapted from software produced by others in the community (see Acknowledgements) and is packaged within RomWBW to provide an integrated solution. A companion document (RomWBW System Guide) provides substantial detail on the architecture and internal operation of this software.

It is worth noting that this software is a perpetual work-in-progress. While it has become fairly stable and robust over time, it is undergoing constant updates to support new and revised hardware produced by the community. Backward compatibility between releases should not be assumed. In order to provide a complete solution, the RomWBW package incorporates a hardware BIOS (hardware drivers) and selected operating systems and application software. All software is derived from the CP/M era of 8-bit computing. The operating systems included have been adapted to run under the RomWBW architecture. In general, application software has simply been included as originally distributed by the

vendors and required no adaptation.

The RomWBW distribution package includes all the tools required to easily build the software from the source that is included in the package. The package includes a range of pre-built ROM and disk images. These are usually sufficient to get your hardware up and running simply by programming a ROM and optionally copying disk image(s) to a floppy disk, CF Card, or SD Card. If you wish to highly customize your system software, it is straightforward to modify the source code and build your own. At present, this requires Microsoft Windows XP or greater. All of the tools have counterparts for Linux, so building the software under Linux should be possible with a little effort.

1.1 System Requirements

RomWBW is purely a software project. It assumes you have a fully functional hardware platform on which to host the software. A great deal of information on procuring and building the appropriate hardware is found on the RetroBrew Computing Wiki at <http://www.retrobrewcomputing.org>. Additionally, the RetroBrew Computing Group has a very active forum found at <http://www.retrobrewcomputing.org/forum>. This forum is the ideal place to ask questions and get guidance for hardware and software. It is the primary forum for supporting RomWBW.

The starting point for a hardware platform that will appropriately host RomWBW software is one of the following CPU boards:

SBC This is the original Z80 CPU board produced by the community. It remains a very functional platform and is relatively easy to build. Note that v1 has a design deficiency that may or may not prevent the proper operation of RomWBW (bank switching does not always function reliably). The SBC CPU board features an ECB bus connector which allows it to be expanded with a backplane and peripheral boards.

Zeta The Zeta is very similar to the SBC board and is generally compatible with it. However, the Zeta platform is optimized to be a compact, standalone system. In addition to the features

of the SBC, it includes an onboard floppy disk controller and the form factor of the board allows it to be mounted directly to a 3.5" floppy disk for a complete computing solution. It optionally supports a single daughter board that provides SD Card storage, VGA Monitor interface, and PS/2 keyboard interface. Although it does not have a bus interface, Zeta is powerful, compact, and fully featured. The Zeta v2 primarily adds enhanced bank switching and an interrupt controller which is not required by RomWBW.

N8 The N8 is a very robust SBC. It is significantly larger than the SBC and incorporates a wide range of peripherals right on the one board (although it also supports expansion via ECB bus). The N8 is based on the Z180 CPU and incorporates interfaces for 2 serial ports, 2 parallel ports, IDE Hard Disk / CF Card, SD Card, sound synthesizer, video display, PS/2 keyboard and mouse interface, and floppy disk controller. This board is very powerful, but more challenging to build. It is not compatible with the SBC/Zeta – it implements a different bank switching mechanism.

Mark IV The Mark IV by John Coffman is similar to the SBC in that it shares the same form factor and ECB for expansion. However, it is substantially more powerful featuring a Z180 CPU and onboard CF Card and SD Card interfaces.

RomWBW fully supports all of the above boards as a starting point. For Zeta, the ParPortProp is supported as an option. The other platforms all support the ECB bus for adding optional peripheral support boards.

In addition to the hardware listed above, RomWBW also runs well on the Microsoft Windows based SIMH Altair Z80 simulator which allows you to try all of the RomWBW features without any actual hardware. The distribution package contains a copy of the simulator software for MS Windows, so it is very easy to use it (see Getting Started).

Note that RomWBW assumes specific board configuration settings. You must ensure that you set the jumpers/switches of each board as required by RomWBW (unless you modify RomWBW and produce a custom version that supports your specific board configurations). The standard board configuration

settings are documented in Appendix A.

Note that RomWBW assumes there is 512KB of ROM and 512KB of RAM for all systems. It is fine if your system has more RAM or ROM than this, but it is problematic if you have less. It would be very rare for a system to have less than these amounts, but be aware of this constraint. These assumptions can be modified via customization later, but the pre-built software must have these minimums.

All of the host boards include a serial port. RomWBW will use this serial port for output when you start your system. By default, RomWBW uses 38,400 baud, 8 data bits, 1 stop bit, and no parity. You will need to connect the primary serial port of the host board to a terminal (or PC running terminal emulation software) to see the system output when you start RomWBW.

The use of the ECB bus signals is standardized such that any ECB add-on board can generally be combined with any of the ECB host boards to provide enhanced functionality. Appendix A provides an inventory of the boards supported by RomWBW along with relevant notes and required board configuration settings. Appendix A also includes a compatibility/support matrix between the host boards and the peripheral support boards.

1.2 Acknowledgements

First, I want to be clear that RomWBW is not the only option available for system software on RetroBrew Computing Z80/Z180 hardware. While many similar projects are no longer active, they are very useful and may contain functionality that has not been incorporated in RomWBW. All of the software projects (including RomWBW) are listed in the RetroBrew Computing Wiki.

The UNA Project from John Coffman is the other currently active software project for the Z80/Z180 projects. It is far more advanced than RomWBW in that it can support all 4 host boards with a single ROM image and allows dynamic system configuration via onboard setup. It does not yet support the full range of hardware or video capabilities of RomWBW. Note that RomWBW supports an UNA "hybrid" configuration in which the UNA BIOS is combined with the RomWBW OS and application layers.

The RetroBrew Computing Group has existed in various forms since about 2010 (?). Many individuals have contributed to the community. The original founder of the community has moved on and requested anonymity going forward. However, his initiative is greatly appreciated. While there is no formal structure to the community, Andrew Bingham has taken the mantle of responsibility for the wiki and discussion group. This is a critical function and he deserves substantial credit for this effort.

Earlier in the community's history, there were multiple branches of software development. Frequently, when a new board was produced, someone would create an independent code branch to support it. This started to lead to a very fragmented set of software that made it very difficult to create an integrated system with selected boards. RomWBW came about as an effort to create a framework that would allow arbitrary hardware to be easily added without creating entirely separate branches of code.

RomWBW essentially became a semi-structured place to incorporate all of the many software efforts of the community. Initially, most of the RomWBW codebase was simply a "cut and paste" of the software produced by others. Over time, much of this software has been repeatedly revised such that it is no longer similar to the original, but RomWBW owes its existence to the contributions of many other individuals. A few of those people are listed below and I apologize for anyone that I may have inadvertently omitted. I have intentionally omitted the original founder of the community based on my understanding of his desire to be anonymous going forward.

Douglas Goodall worked in very close collaboration with me during the first year of the RomWBW Project. He produced an excellent set of supporting utility programs and provided a great deal of design input. Regrettably, his utilities no longer have a caretaker and have become unusable as RomWBW has evolved, but their legacy continues within the current codebase. The source for all of these utilities is still available if anyone wants to take responsibility for bringing them back to current status.

John Coffman has personally produced a great deal of the hardware designs within the community. RomWBW contains many portions of code that John contributed over time. Additionally, he has been instrumental in providing advice and guidance to me for many years now.

Dan Werner has been one of the most prolific coders within the community. A

great deal of his code was incorporated in the early RomWBW releases. David Giles produced some code that also provided a more integrated set of software for each host board. Over time, much of his code was incorporated in RomWBW. Likewise, Max Scane has produced code that ultimately wound up in RomWBW – specifically, he contributed the CLRDIR application.

It is my belief that all code incorporated into RomWBW has been done so with the express or implied permission of the original authors. I realize there have been many other individuals that have contributed to RomWBW and apologize for not naming all of them.

1.3 RomWBW Distribution Package

RomWBW is distributed as a complete package (a .zip file) that contains everything appropriate for the different hardware variations. In other words, don't look for a specific distribution for your hardware, you just want the current package. Within the package, you will find documentation, source code, build tools, and pre-built ROM and disk images.

The distribution package is usually hosted at the following locations:

- **RetroBrew Computing Wiki**

Navigate to <https://www.retrobrewcomputers.org>. Then, using the navigation menu on the left, choose software → firmwareos → romwbw to reach the RomWBW Project Page. At the bottom of the page you will find the distribution files listed for download.

- **GitHub**

Navigate to <https://github.com/wwarthen/RomWBW> for the RomWBW Project on GitHub. Select "releases" to reach the list of distribution files. Note that you will see both Prereleases and Releases listed. Unless you specifically want to test work-in-progress, please download only a Release version.

The package should be named something like RomWBW-2.8-Package.zip. Using any standard personal computer (Windows, Linux, Mac, etc.), download and

extract the contents of the zip file using any of the standard zip tools. You will see that there are several directories that are used to organize the contents. Don't get overwhelmed. Initially, all you really care about is the Output directory (and possibly the Doc directory):

Doc Contains documentation files for many components of the RomWBW distribution including operating systems, applications, and other aspects of RomWBW itself. In most cases, the name of the file should identify the component being documented.

Hardware Files that are specific to certain hardware components. For example, it has the font ROM images for the video display boards. You do not need any of these files for the host boards used initially. Appendix A describes the contents of this directory for relevant boards.

Images Files that are used to create disk images. Since the disk images are all pre-built, you do not need to worry about this directory until you want to create custom disk images (documented later).

Output The ROM and Disk Images that you need to get started as documented below in Getting Started.

Source The source code files that are compiled or assembled to create RomWBW. Again, the output is pre-built, so you don't need to worry about this directory until you want to customize your system.

Tools Windows-based applications that are used to build RomWBW. It also contains applications that you can use to copy disk images to floppy disks, CF Cards, SD Cards, etc. It also has the SIMH simulation software.

In most cases, you will find a ReadMe.txt file in the directory which describes the contents of the directory in more detail.

Section 2

Getting Started

Because of the wide variety of hardware combinations, there is no "one case fits all" approach to getting started. The good news is that RomWBW operates very consistently regardless of the specific hardware. The operating systems, applications, and storage formats are all common. However, building and testing your hardware is entirely outside the scope of this document. The RetroBrew Computing Forum (<https://www.retrobrewcomputers.org/forum>) is probably the best place to get advice if you get stuck on hardware issues.

2.1 SIMH Simulator

It is not necessary, but I highly recommend running RomWBW under the SIMH Simulator as a first step. This requires no hardware and will allow you to see how it should look when you use it on real hardware. Since the SIMH software is included in the distribution package, you can start it with a single command. Using a command prompt window, navigate to the high level directory of the distribution. Enter the command "sim" and the simulator should start up. The first few lines of output should look similar to Figure 2.1. You may see some benign warning messages that can be ignored.

```

1 ROM Image: 'Output\SBC_simh.rom'
2
3 RetroBrew HBIOS v2.8.0-pre.5, 2016-06-22
4
5 SBC Z80 @ 20.000MHz ROM=512KB RAM=512KB
6 UART0: IO=0x68 8250 MODE=38400,8,N,1
7 SIMRTC: Wed 2016-06-22 15:10:17
8 MD: UNITS=2 ROMDISK=384KB RAMDISK=384KB
9 HDSK: UNITS=2
10
11 <Additional Output Truncated>

```

Figure 2.1: SIMH Boot Example

2.2 Board Setup

In all cases, you will want to start with a Z80/Z180 host board. Any of the boards listed in System Requirements will work fine. I strongly recommend that you initially work on getting just the single host board running by itself – don't even plug it into an ECB backplane.

Given a host board that is assembled and passes any hardware checks recommended by the boards designer, you need to make sure the board is configured for RomWBW. Refer to the entry in Appendix A for your board and confirm that all switches and jumpers on the board are set as required by RomWBW.

Your initial goal is to locate and program a ROM image for your host board. The ROM images are located in the Output directory. You are looking for the files that end in ".rom". Don't worry about all of the other variations at this point. Refer to Table 2.1 to determine the ROM image that you want.

CPU Board	ROM Image File
SBC v1/v2	SBC_std.rom
Zeta v1	ZETA_std.rom
Zeta v2	ZETA2_std.rom
N8 (2511)	N8_2511.rom
N8 (2312)	N8_2312.rom
Mark IV	MK4_std.rom

Table 2.1: Basic ROM Files

Locate the appropriate ROM image file in the Output directory based on Table

2.1. You should see that the file is exactly 512KB in size. As indicated above in System Requirements, your system should have a ROM capacity of 512KB or greater. You need to program the file to your ROM using whatever tool you have. Programming a ROM chip is beyond the scope of this document, but any feel free to ask for help at the RetroBrew Computing Forum. The ROM image files are pure binary and should be programmed into the ROM chip starting at address 0H thru address 7FFFH (8000H bytes). Insert the programmed ROM chip in your system.

Initially, you will need two external connections to your board. Power and serial port. All of the CPU boards provide an onboard power connection. Refer to the board designer's notes on the RBC Wiki for more information on the power connection your board requires.

Finally, you must connect the primary serial port of your host board to a terminal using 38,400 baud, 8 data bits, 1 stop bit, and no parity. You can use either a dedicated terminal or use terminal emulation software on your PC¹. When connecting to a standard PC serial port, a null modem cable is required. There is a good document on the Wiki that explains cabling of serial ports at <http://????????>. [TODO: Need to restore serial port cabling document on Wiki!]

2.3 Startup

System startup (booting) is accomplished simply by applying power. In some cases, it may be necessary to press the reset button after applying power to get a successful startup.

If everything is working properly, you should see something like Figure 2.2 on your terminal screen. Your output will vary somewhat depending on your specific hardware. The example shown comes from a Mark IV.

If you see output on your terminal screen, but it is garbled/unreadable, then check the serial port configuration settings on your terminal or terminal emulation software.

¹Under Windows, Tera Term is a good choice for terminal emulation.


```

1  RetroBrew HBIOS v2.8.0-pre.5, 2016-06-03
2
3  MARK IV Z180 @ 18.432MHz ROM=512KB RAM=512KB
4  ASCI0: IO=0x46,48 MODE=38400,8,N,1
5  ASCI1: IO=0x47,49 MODE=38400,8,N,1
6  DSRTC: MODE=STD Wed 2016-06-22 15:03:06
7  MD: UNITS=2 ROMDISK=384KB RAMDISK=384KB
8  IDE: MODE=MK4 IO=0x80 UNITS=2
9  IDE0: NO MEDIA
10 IDE1: NO MEDIA
11 SD: MODE=MK4 FAST OPR=0x89 CNTR=0x4A TRDR=0x4B UNITS=1
12 SD0: NO MEDIA
13
14 Unit          Device      Type          Capacity/Mode
15 -----
16 Disk 0        MD1:         RAM Disk      384KB,LBA
17 Disk 1        MD0:         ROM Disk      384KB,LBA
18 Disk 2        IDE0:         Hard Disk     --
19 Disk 3        IDE1:         Hard Disk     --
20 Disk 4        SD0:         SD Card       --
21 Serial 0      ASCI0:        RS-232        38400,8,N,1
22 Serial 1      ASCI1:        RS-232        38400,8,N,1
23
24 MARK IV Z180 Boot Loader
25
26 Boot: (C)PM, (Z)System, (M)onitor,
27       (L)ist disks, or Disk Unit # ==>

```

Figure 2.2: Typical Boot Display

If you do not see any output of any kind on your terminal screen, the following general areas should be checked:

- Confirm power is being applied to the board and the the voltage is in an acceptable range.
- Confirm the ROM is programmed accurately by placing it back in the programmer and using the verify function.
- Verify the serial connection. When connecting to a PC, make sure you have a null modem cable or adapter.
- Review your board's construction carefully for chip orientation, bent pins, missing or bridged solder joints, etc.

You will find that the RetroBrew Computing Group is very helpful if you get stuck. The best way to request assistance is to post a message on the Forum.

2.4 Boot Display

As illustrated in Figure 2.2, RomWBW displays a lot of information about the system and its configuration. There are 4 basic sections to the boot display.

Line 1 is a banner that identifies the BIOS portion of the ROM including version and build date.

Lines 3-12 display the hardware inventory of the system as understood by the ROM. Note that some of this information is *not* discovered dynamically – it is built into the ROM. So, do not be alarmed if some parts of this display do not match your hardware. For example, the RAM and ROM size are configured into the ROM itself. You can refer to Appendix A for more information on how to read the specific lines.

Lines 14-22 contain a table that summarizes the devices in the system. This information is used when the operating system is loaded/configured to assign OS devices to system devices.

Lines 24-27 is the display of the boot loader menu and prompt. The boot loader allows you to choose the operating mode you want to initiate. These options will be described the next section.

2.5 Loader

At the conclusion of a successful system startup, the loader menu/prompt will be displayed on the console. The function of the loader is to load an operating system or system monitor.

2.5.1 Monitor

Pressing 'M' at the boot loader prompt will launch a basic system monitor. The system monitor provides very basic functions that are primarily useful for testing components of your system. These functions include displaying and modifying

memory, reading and writing to I/O ports, etc.

Refer to ??? for monitor operation.

2.5.2 CP/M

Pressing 'C' at the boot loader prompt will launch Digital Research CP/M-80 Version 2.2. A complete copy of the CP/M operating system is imbedded in the ROM and will be loaded directly from there, so no disk access is required.

Initially, drive A will be a RAM drive (initialized with no files). Drive B will be a ROM drive. The standard CP/M distribution files are included on the ROM drive (e.g., ASM, PIP, STAT). Drive B will initially be the logged drive. At this point, you can execute the programs on drive B. Remember that drive B is a ROM drive, so any attempt to write to that drive will result in an error.

Refer to Chapter ?? for more information on using CP/M 2.2.

2.5.3 Z-System

Pressing 'Z' at the boot loader prompt will launch Z-System, a CP/M 2.2 compatible operating system with many enhancements. As with CP/M, this operating system will be loaded directly from ROM.

The drive configuration for Z-System is identical to CP/M.

Refer to Chapter ?? for more information on using Z-System.

2.5.4 Disk Boot

The boot loader also supports loading an operating system from a disk device. In this case, you must press the number key corresponding to the disk device containing the operating system to be loaded. The disk device numbers are the ones listed in the device summary table.

In order to boot from a disk device, it must be properly initialized using the SYSCOPY application or equivalent. Attempting to boot a disk that has no operating system will result in an error and the boot loader prompt will be redisplayed.

You can press 'L' at the boot loader prompt to display a list of the disk devices available. The existence of a disk in this list does *not* mean that it has been initialized with an operating system.

Section 3

Disk Usage

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3.1 Preparing Disk Media

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lectus tellus, aliquet aliquam, luctus a, egestas a, turpis. Mauris lacinia lorem sit amet ipsum. Nunc quis urna dictum turpis accumsan semper.

3.2 Disk Images

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3.3 Placing Operating System on Disk

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Section 4

System Monitor

Lorem ipsum dolor sit amet, consectetur adipiscing elit. Etiam lobortis facilisis sem. Nullam nec mi et neque pharetra sollicitudin. Praesent imperdiet mi nec ante. Donec ullamcorper, felis non sodales commodo, lectus velit ultrices augue, a dignissim nibh lectus placerat pede. Vivamus nunc nunc, molestie ut, ultricies vel, semper in, velit. Ut porttitor. Praesent in sapien. Lorem ipsum dolor sit amet, consectetur adipiscing elit. Duis fringilla tristique neque. Sed interdum libero ut metus. Pellentesque placerat. Nam rutrum augue a leo. Morbi sed elit sit amet ante lobortis sollicitudin. Praesent blandit blandit mauris. Praesent lectus tellus, aliquet aliquam, luctus a, egestas a, turpis. Mauris lacinia lorem sit amet ipsum. Nunc quis urna dictum turpis accumsan semper.

4.1 Monitor Commands

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Section 5

Operating Systems

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5.1 CP/M-80

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5.2 Z-System

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5.3 NZ-COM

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Section 6

Features

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6.1 Real Time Clock

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6.2 Directory Time Stamping

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Section 7

Customization

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7.1 Build Process

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lectus tellus, aliquet aliquam, luctus a, egestas a, turpis. Mauris lacinia lorem sit amet ipsum. Nunc quis urna dictum turpis accumsan semper.

7.2 Configuration File

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Section 8

Formatting Samples

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An itemized list:

- First list item
- Second list item
- Third list item

An enumerated list:

1. First list item
2. Second list item

3. Third list item

A description list:

Ant What is an ant?

Elephant Lorem ipsum dolor sit amet, consectetur adipiscing elit. Etiam lobortis facilisis sem. Nullam nec mi et neque pharetra sollicitudin. Praesent imperdiet mi nec ante. Donec ullamcorper, felis non sodales commodo, lectus velit ultrices augue, a dignissim nibh lectus placerat pede. Vivamus nunc nunc, molestie ut, ultricies vel, semper in, velit. Ut porttitor. Praesent in sapien. Lorem ipsum dolor sit amet, consectetur adipiscing elit. Duis fringilla tristique neque. Sed interdum libero ut metus. Pellentesque placerat. Nam rutrum augue a leo. Morbi sed elit sit amet ante lobortis sollicitudin. Praesent blandit blandit mauris. Praesent lectus tellus, aliquet aliquam, luctus a, egestas a, turpis. Mauris lacinia lorem sit amet ipsum. Nunc quis urna dictum turpis accumsan semper.

12345

Here is an output sample (Verbatim):

```

1 ROM Image: 'Output\SBC_simh.rom'
2
3 RetroBrew HBIOS v2.8.0-pre.5, 2016-06-22
4
5 SBC Z80 @ 20.000MHz ROM=512KB RAM=512KB
6 UART0: IO=0x68 8250 MODE=38400,8,N,1
7 SIMRTC: Wed 2016-06-22 15:10:17
8 MD: UNITS=2 ROMDISK=384KB RAMDISK=384KB
9 HDSK: UNITS=2
10
11 Below line is 80 characters:
12
13 12345678901234567890123456789012345678901234567890123456789012345678901234567890

```

Figure 8.1: Sample Output

Table 8.1 is an example of a floating table:

CPU Board	ROM Image File
SBC v1/v2	SBC_std.rom
Zeta v1	ZETA_std.rom
Zeta v2	ZETA2_std.rom
N8 (2511)	N8_2511.rom
N8 (2312)	N8_2312.rom
Mark IV	MK4_std.rom

Table 8.1: Sample Table

Appendix A

Board Notes

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A.1 SBC

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lectus tellus, aliquet aliquam, luctus a, egestas a, turpis. Mauris lacinia lorem sit amet ipsum. Nunc quis urna dictum turpis accumsan semper.

A.2 Zeta

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A.3 N8

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A.4 Mark IV

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Appendix B

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B.1 GNU General Public License

Version 3, 29 June 2007

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